IN THE CLAIMS

Please amend Claims 1 and 3-10 to read as follows:

1. (Amended) A method of driving an AC plasma display panel, wherein said AC plasma display panel comprises:

an address electrode including t (t: integer of at least 2) strip portions, which are extended in a first direction and arranged in a second direction perpendicular to said first direction, and which are electrically connected in common;

t discharge cells belonging to said t strip portions respectively;

a scan electrode including t strip portions belonging to said t discharge cells in one-to-one correspondence and arranged to grade-separately intersect with said strip portions of said address electrode;

a sustain electrode including t strip portions belonging to said t discharge cells in one-to-one correspondence and paired with said strip portions of said scan electrode; and

a dielectric substance covering at least one of said scan electrode and said sustain electrode,

said method comprising applying a prescribed voltage to said strip portions of said address electrode in common, applying a prescribed voltage to each strip portion of said scan electrode, and applying a first voltage to one of said t strip portions of said sustain electrode belonging to a single discharge cell among said t discharge cells while applying a second voltage, which is different from said first voltage, to remaining all of said strip portions of said sustain electrode, for forming desired discharge only in said single discharge cell.

3. (Amended) The method of driving an AC plasma display panel according to claim 1, wherein

a first potential difference between said strip portion of said sustain electrode supplied with said first voltage and said strip portion of said scan electrode paired with said strip portion supplied with said first voltage is larger than a second potential difference between said strip portion of said sustain electrode supplied with said second voltage and said strip portion of said scan electrode paired with said strip portion supplied with said second voltage.

4. (Amended) The method of driving an AC plasma display panel according to claim 3, wherein

said second potential difference is substantially zero.

5. (Amended) The method of driving an AC plasma display panel according to claim l, wherein

said first voltage is successively applied to one of said t strip portions of said sustain electrode while said second voltage is applied to remaining all of said strip portions of said sustain electrode in a period when said prescribed voltage is applied to said scan electrode.

6. (Amended) The method of driving an AC plasma display panel according to claim l, wherein

said AC plasma display panel has a plurality of said scan electrodes and a plurality of said sustain electrodes respectively,

in a period for applying said first voltage to each one of said t strip portions of each of said plurality of sustain electrodes in common, said prescribed voltage is successively applied to one of said strip portions of said scan electrodes paired with said strip portions that are being supplied with said first voltage.

7. (Amended) The method of driving an AC plasma display panel according to claim 6, further comprising:

forming, after said period, first auxiliary discharge in said discharge cell to which said strip portion of said sustain electrode supplied with said second voltage in said period belongs between strip portions of said scan electrode and said address electrode.

8. (Amended) The method of driving an AC plasma display panel according to claim 6, further comprising:

forming, after said period, second auxiliary discharge in said discharge cell selected and supplied with said first voltage for forming said desired discharge in said period between strip portions of said scan electrode and said sustain electrode.

a7 cont

9. (Amended) An AC plasma display panel comprising:

an address electrode including t (t: integer of at least 2) strip portions;

t discharge cells, having discharge gaps capable of forming desired discharge, belonging to said t strip portions respectively;

a scan electrode including t strip portions belonging to said t discharge cells in one-to-one correspondence and arranged to grade-separately intersect with said strip portions of said address electrode;

a sustain electrode including t strip portions belonging to said t discharge cells in one-to-one correspondence and paired with said strip portions of said scan electrode;

a dielectric substance covering at least one of said scan electrode and said sustain electrode; and

a plurality of non-discharge cells, having non-discharge gaps harder to form discharge than said discharge gaps, arranged on a same plane and belonging to said address electrode, wherein

said t discharge cells are arranged on said same plane and arranged adjacently to each other through at least one said non-discharge cell at least in a direction parallel to a display line,

said AC plasma display panel further comprising:

a plurality of barrier ribs separating said non-discharge cells from said discharge cells or said non-discharge cells at least along a direction intersecting with said display line, wherein

at least two adjacent ones of said strip portions of said address electrode are integrated with each other extending over said non-discharge cells and said discharge or non-discharge cells separated by said barrier ribs.

a1 cont

10. (Amended) A plasma display device comprising:

an AC plasma display panel; and

a driving unit for said AC plasma display panel, wherein

said AC plasma display panel comprises:

an address electrode including t (t: integer of at least 2) strip portions, which are extended in a first direction and arranged in a second direction perpendicular to said first direction, and which are connected to an output terminal of said driving unit in common;

t discharge cells belonging to said t strip portions respectively;